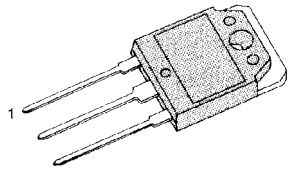
SGH80N60UFD

N-CHANNEL IGBT

FEATURES

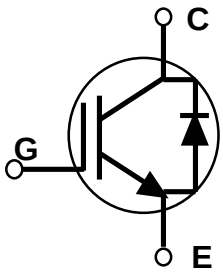
* High Speed Switching
* Low Saturation Voltage
  : V_{CE}(sat) = 2.0 V (@ Ic=40A)
* High Input Impedance
*CO-PAK, IGBT with FRD
  : Trr = 50nS (typ.)

TO-3P



APPLICATIONS

* AC & DC Motor controls
* General Purpose Inverters
* Robotics , Servo Controls
* Power Supply
* Lamp Ballast



ABSOLUTE MAXIMUM RATINGS

Symbol	Characteristics	Rating	Units
V_{CES}	Collector-Emitter Voltage	600	V
V_{GES}	Gate-Emitter Voltage	±20	V
I_C	Collector Current @ Tc = 25°C	80	A
	Collector Current @ Tc = 100°C	40	A
$I_{CM\ (1)}$	Pulsed Collector Current	220	A
I_F	Diode Continuous Forward Current @ Tc = 100°C	25	A
I_{FM}	Diode Maximum Forward Current	280	A
P_D	Maximum Power Dissipation @Tc = 25°C	195	W
	Maximum Power Dissipation @Tc = 100°C	78	W
Tj	Operating Junction Temperature	-55 ~ 150	°C
Tstg	Storage Temperature Range	-55 ~ 150	°C
T_L	Maximum Lead Temp. For Soldering Purposes, 1/8” from case for 5 seconds	300	°C

Notes:(1) Repetitive rating : Pulse width limited by max. junction temperature

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ELECTRICAL CHARACTERISTICS (IGBT PART)
(T_C=25°C,Unless Otherwise Specified)

Symbol	Characteristics	Test Conditions	Min	Typ	Max	Units
BV_{CES}	C - E Breakdown Voltage	V_{GE} = 0V , I_C = 250uA	600	-	-	V
$\Delta V_{CES}/\Delta T_J$	Temperature Coeff. of Breakdown Voltage	V_{GE} = 0V , I_C = 1mA	-	0.6	-	V/°C
$V_{GE(th)}$	G - E threshold voltage	I_C = 40mA , V_{CE} = V_{GE}	4.5	5.5	7.5	V
I_{CES}	Collector cutoff Current	V_{CE} = V_{CES} , V_{GE} = 0V	-	-	250	uA
I_{GES}	G - E leakage Current	V_{GE} = V_{GES} , V_{CE} = 0V	-	-	100	nA
V_{CE}(sat)	Collector to Emitter saturation voltage	Ic=40A, V_{GE} = 15V	-	2.0	2.6	V
		Ic=80A, V_{GE} = 15V	-	2.6	-	V
Cies	Input capacitance	V_{GE} = 0V , f = 1MHz	-	2790	-	pF
Coes	Output capacitance	V_{CE} = 30V	-	347	-	pF
Cres	Reverse transfer capacitance		-	96	-	pF
td(on)	Turn on delay time	V_{CC} = 300V , I_C = 40A	-	17	-	ns
tr	Turn on rise time	V_{GE} = 15V	-	33	-	ns
td(off)	Turn off delay time	R_G = 5Ω	-	97	130	ns
tf	Turn off fall time	Inductive Load	-	70	140	ns
Eon	Turn on Switching Loss		-	0.12	-	mJ
Eoff	Turn off Switching Loss		-	0.68	-	mJ
Ets	Total Switching Loss		-	0.8	1.5	mJ
Qg	Total Gate Charge	Vcc = 300V	-	178	267	nC
Qge	Gate-Emitter Charge	V_{GE} = 15V	-	40	60	nC
Qgc	Gate-Collector Charge	Ic = 40A	-	49	74	nC
Le	Internal Emitter Inductance	Measured 5mm from PKG	-	14	-	nH

ELECTRICAL CHARACTERISTICS (DIODE PART)
(T_C=25°C,Unless Otherwise Specified)

Symbol	Characteristics	Test Conditions		Min	Typ	Max	Units
V_{FM}	Diode Forward Voltage	I_F=25A	Tc =25°C	-	1.4	1.7	V
			Tc =100°C	-	1.3	-	
Trr	Diode Reverse Recovery Time	I_F=25A, V_R=200V -di/dt=200A/uS	Tc =25°C	-	50	75	nS
			Tc =100°C	-	105	-	
Irr	Diode Peak Reverse Recovery Current		Tc =25°C	-	4.5	10	A
			Tc =100°C	-	8.5	-	
Qrr	Diode Reverse Recovery Charge		Tc =25°C	-	112	375	nC
			Tc =100°C	-	420	-	

THERMAL RESISTANCE

Symbol	Characteristics	Min	Typ	Max	Units
$R_{\theta}JC$	Junction-to-Case (IGBT)	-	-	0.64	°C/W
$R_{\theta}JC$	Junction-to-Case (DIODE)	-	-	0.83	°C/W
$R_{\theta}JA$	Junction-to-Ambient	-	-	40	°C/W
$R_{\theta}CS$	Case-to-Sink	-	0.24	-	°C/W

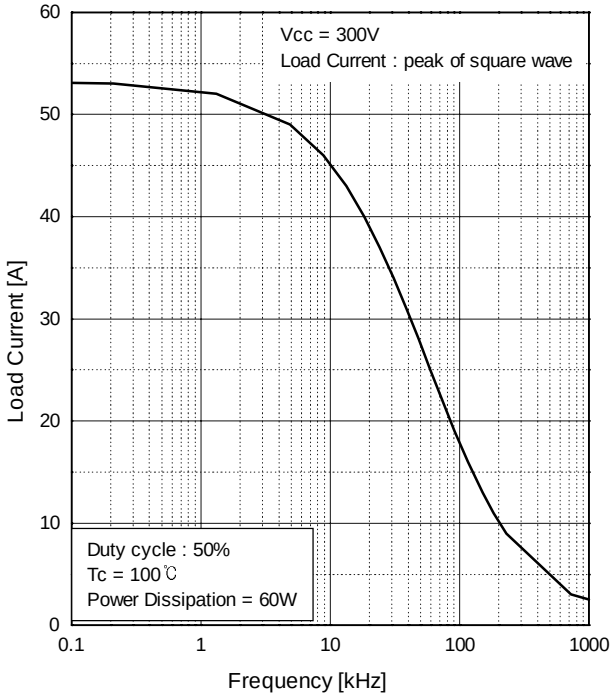

Fig.1 Typical Load Current vs. Frequency

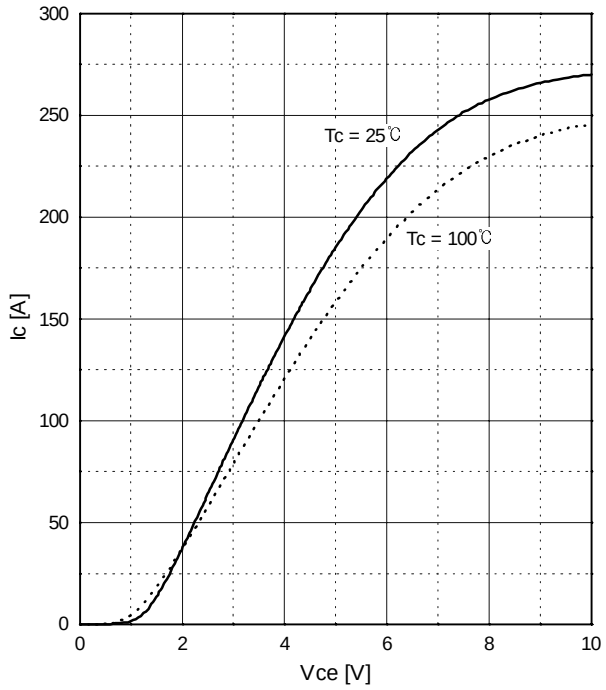

Fig.2 Typical Output Characteristics

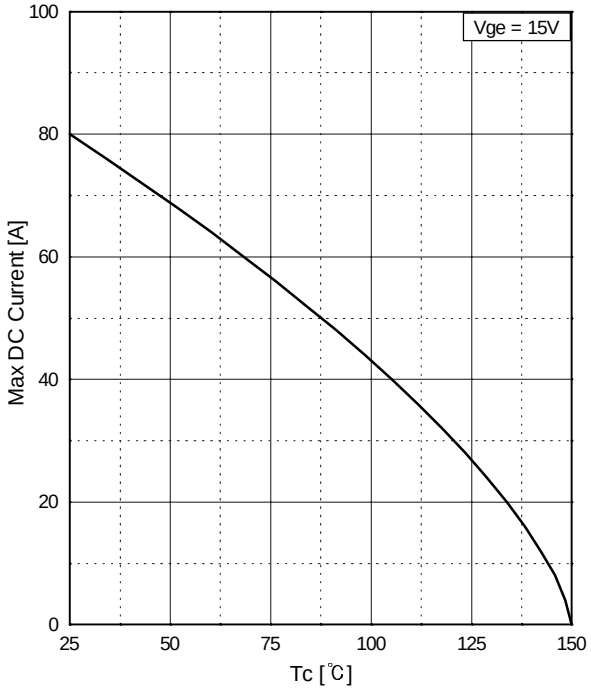

Fig.3 Maximum Collector Current vs. Case Temperature

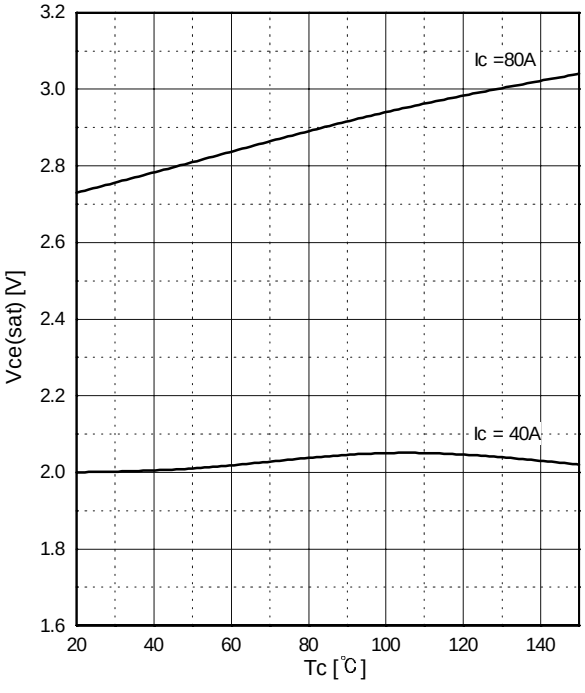

Fig.4 Collector to Emitter Voltage vs. Case Temperature

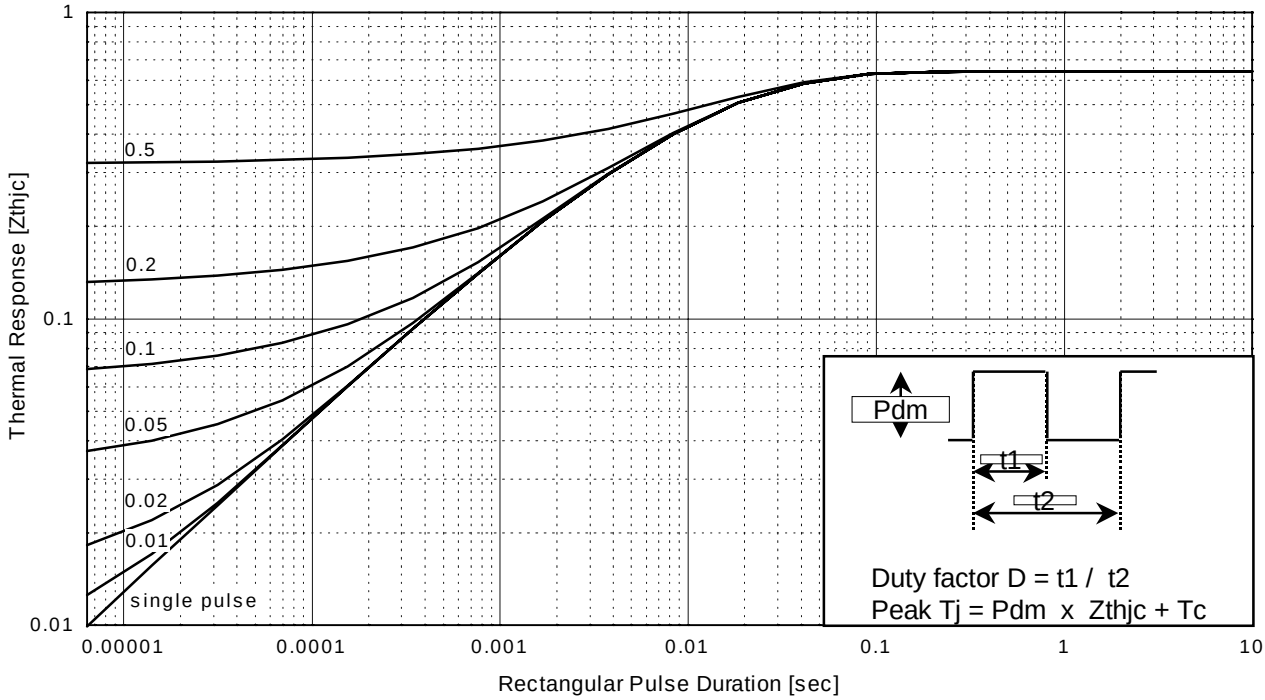

Fig.5 Maximum Effective Transient Thermal Impedance, Junction to Case

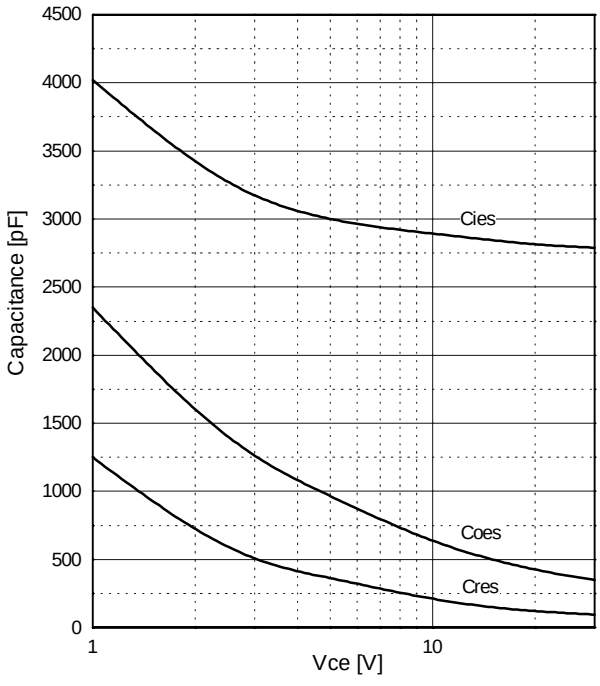

Fig.6 Typical Capacitance vs.
Collector to Emitter Voltage

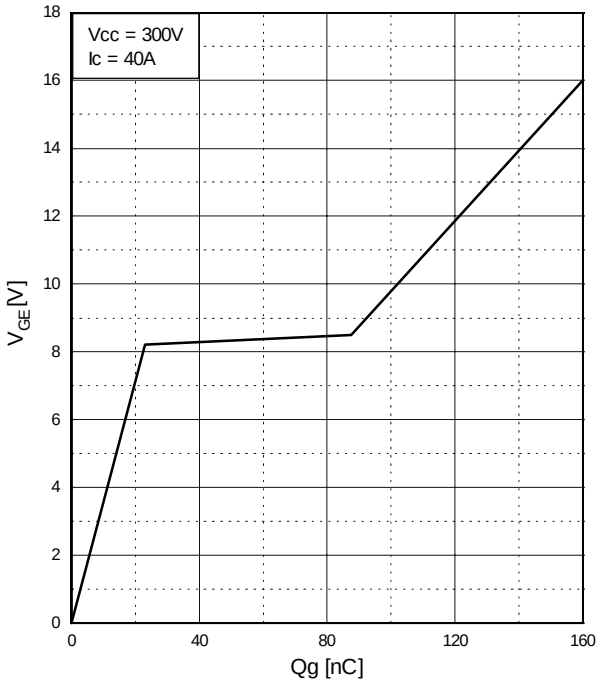

Fig.7 Typical Gate Charge vs.
Gate to Emitter Voltage

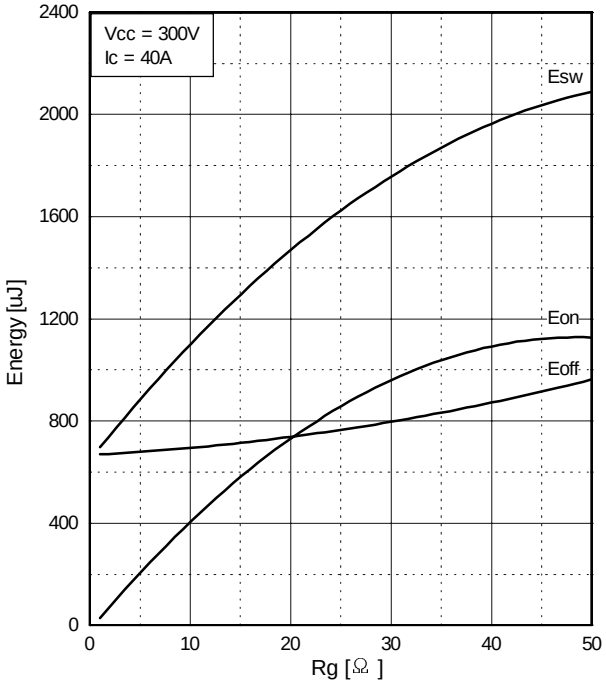

Fig.8 Typical Switching Loss vs. Gate Resistance

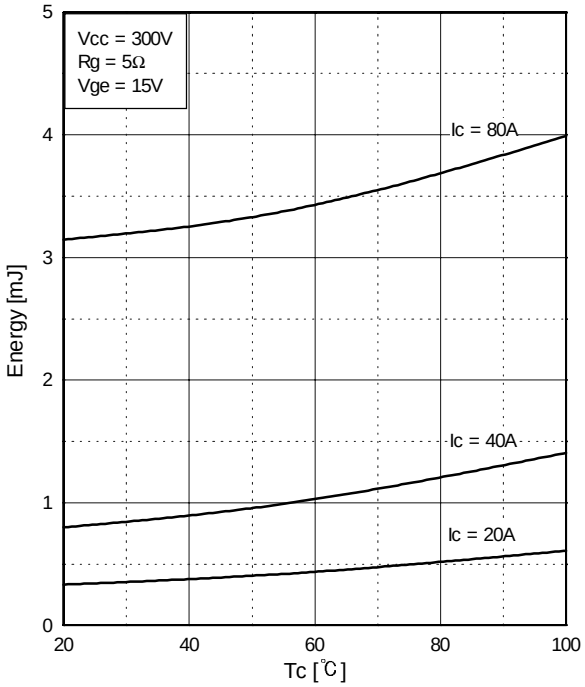

Fig.9 Typical Switching Loss vs. Case Temperature

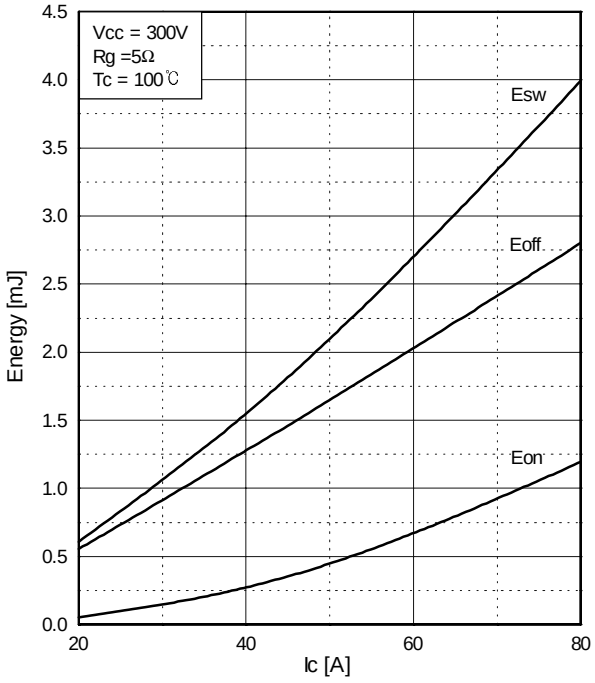

Fig.10 Typical Switching loss vs. Collector to Emitter Current

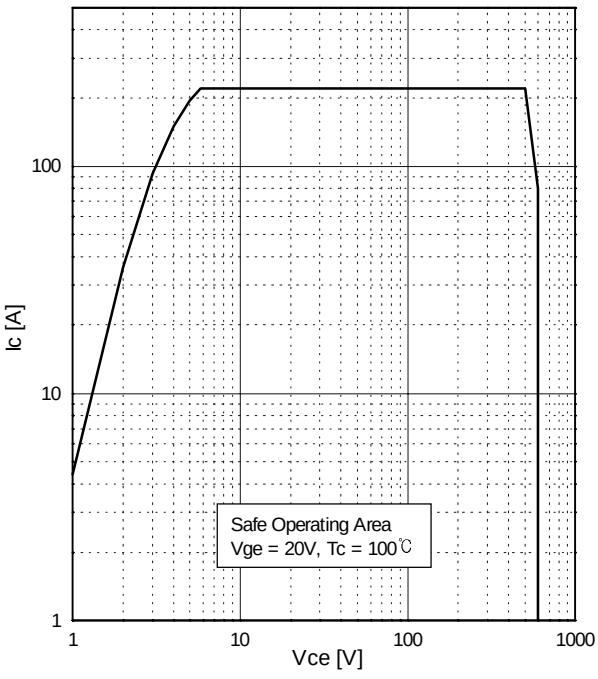

Fig.11 Turn-off SOA

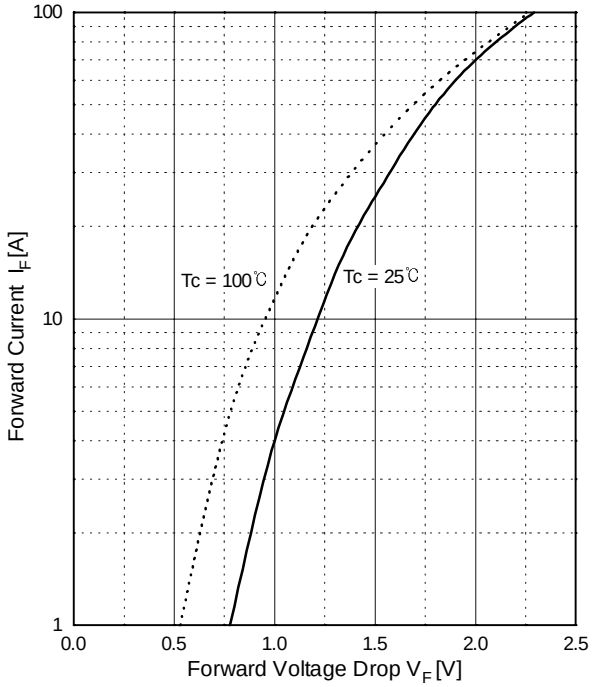

Fig.12 Typical Forward Voltage Drop vs. Forward Current

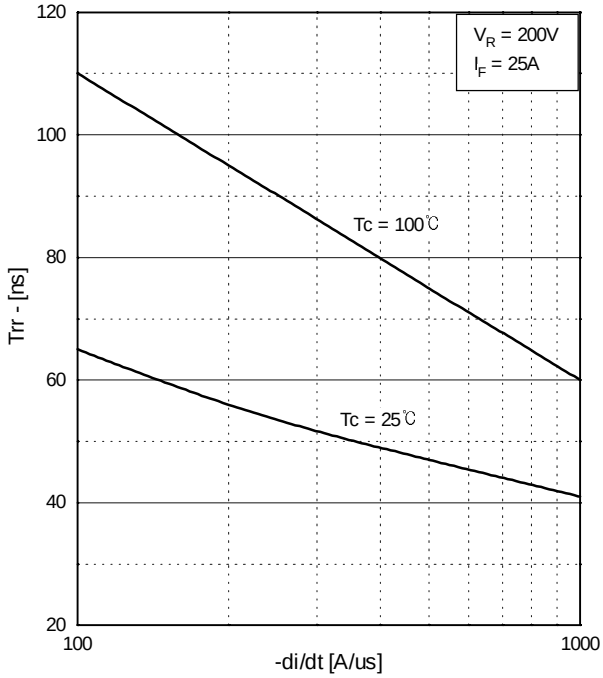

Fig.13 Typical Reverse Recovery Time vs. di/dt

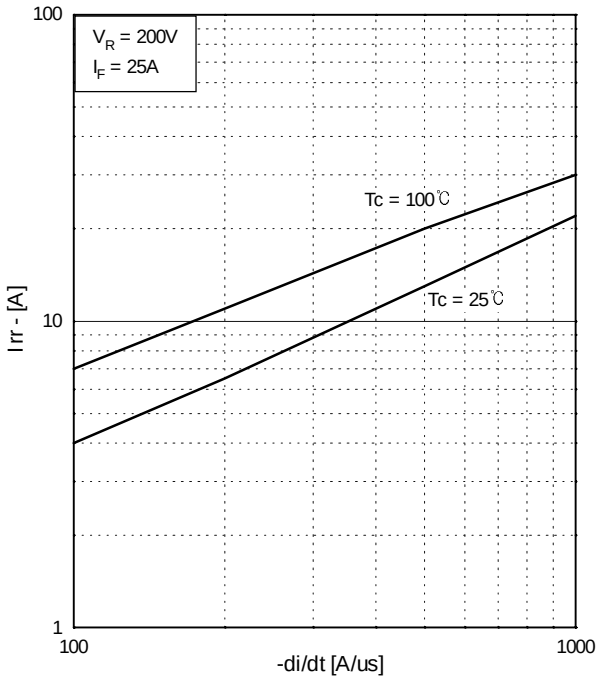

Fig.14 Typical Reverse Recovery Current vs. di/dt

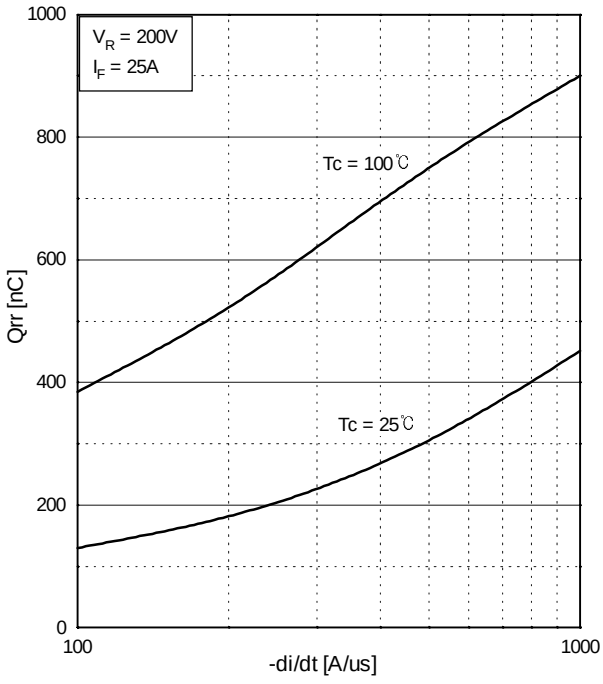

Fig.15 Typical Stored Charge vs. di/dt

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